

## **AMENDMENTS TO THE SPECIFICATION**

Please delete the first two paragraphs in page 21 in their entirety and replace with the following:

As shown in Fig. 5, an oxide film 141 of about 20 nm in thickness and a nitride film 142 of about 200 nm in thickness are successively deposited on the SOI substrate for thereafter patterning isolation regions through a mask of a patterned resist film ~~[[142]]~~ **143** and etching the triple multilayer film of the nitride film 142, the oxide film 141 and the SOI layer 3 to leave the lower layer part of the SOI layer 3, thereby forming a plurality of partial trenches 144.

Then, an oxide film of about 500 nm is deposited so that a structure formed with the partial oxide films 31 and the SOI layer 3 (the well regions 11 and 12) located under the same can be obtained by polishing the nitride film 142 to an intermediate ~~[[portion]]~~ **portion** by CMP treatment by a method similar to that in general trench isolation and thereafter removing the nitride film 142 and the oxide film 141, as shown in Fig. 6.